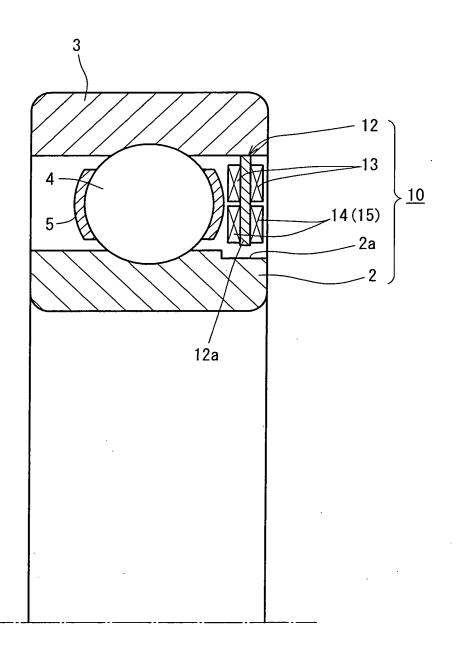
F I G. 1



F I G. 2

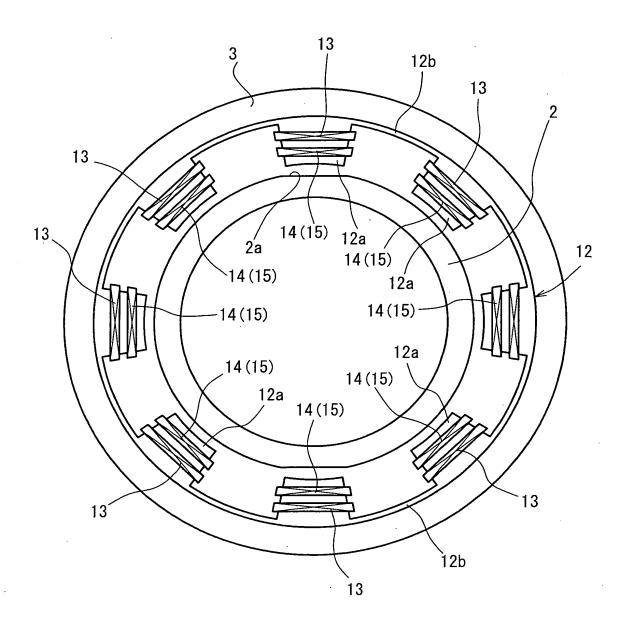
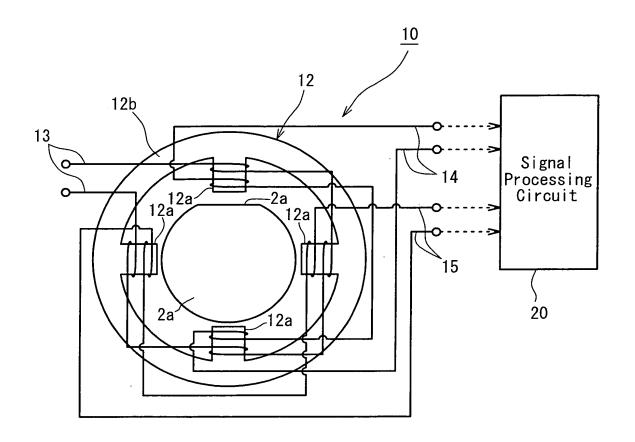
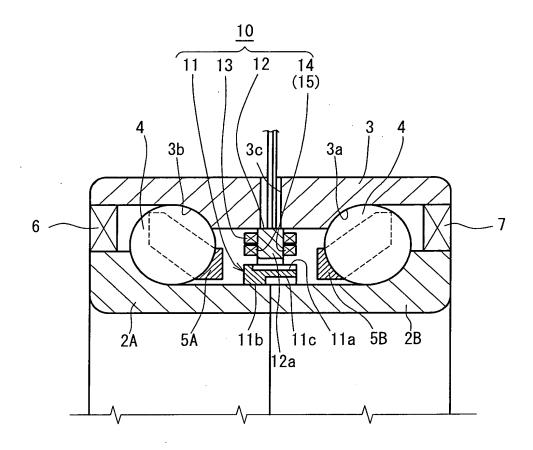


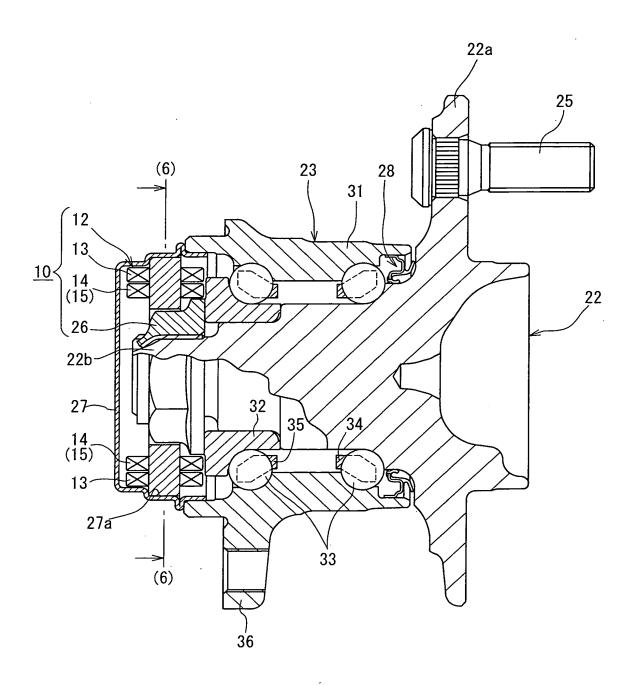
FIG. 3



F I G. 4



F I G. 5



F I G. 6

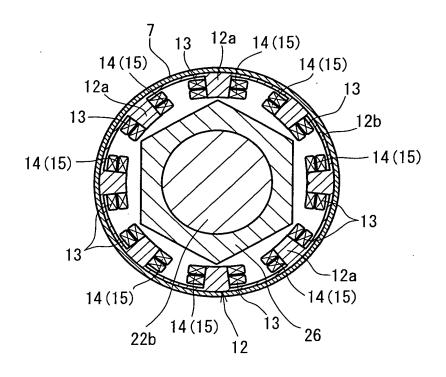


FIG. 7

12b

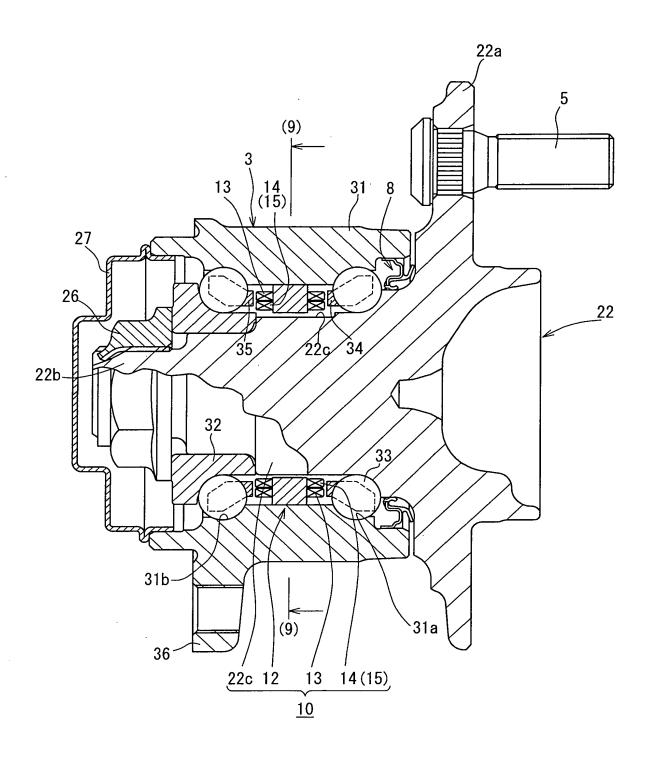
12c

Signal Processing Circuit

15

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F I G. 8



F I G. 9

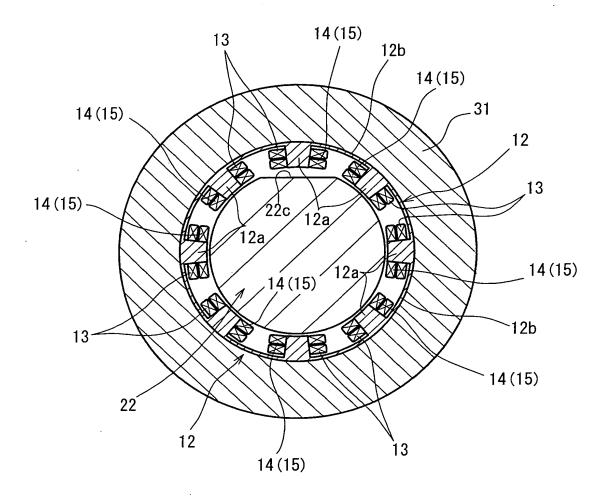
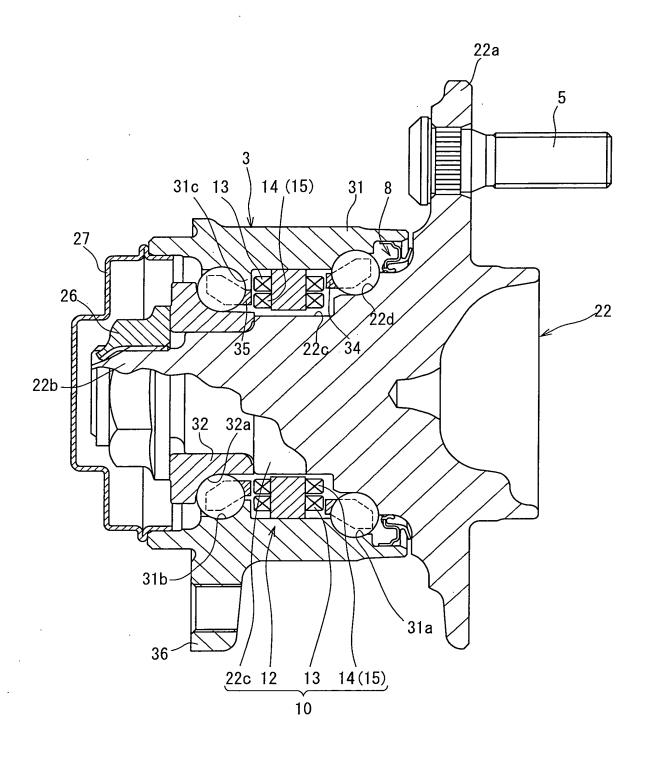


FIG. 10



F I G. 11

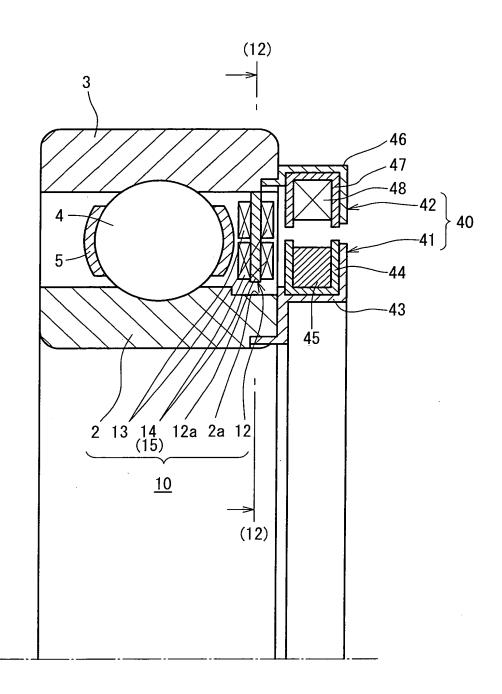
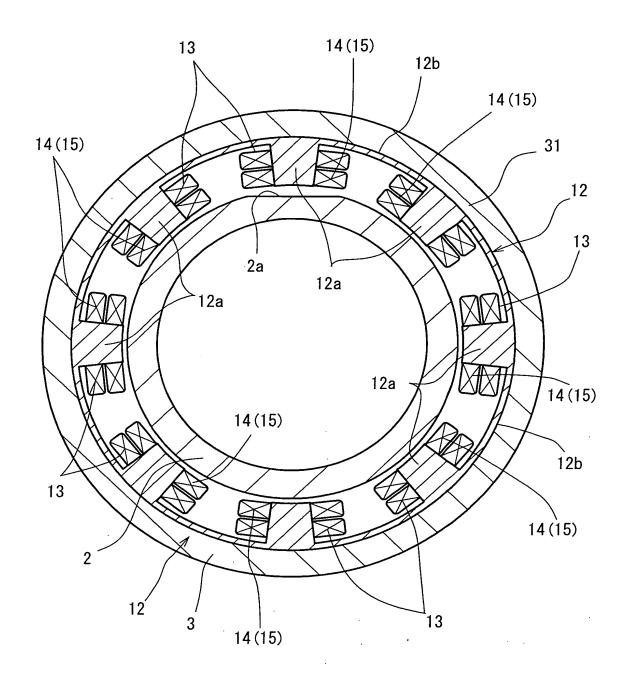


FIG. 12



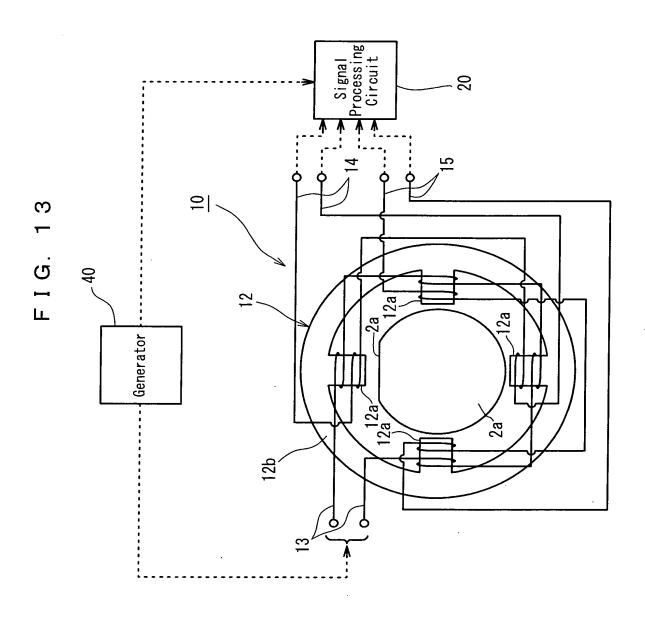


FIG. 14

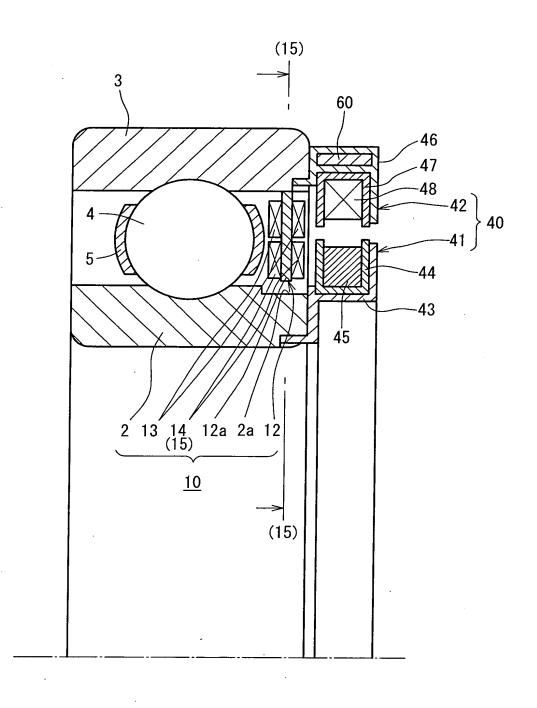
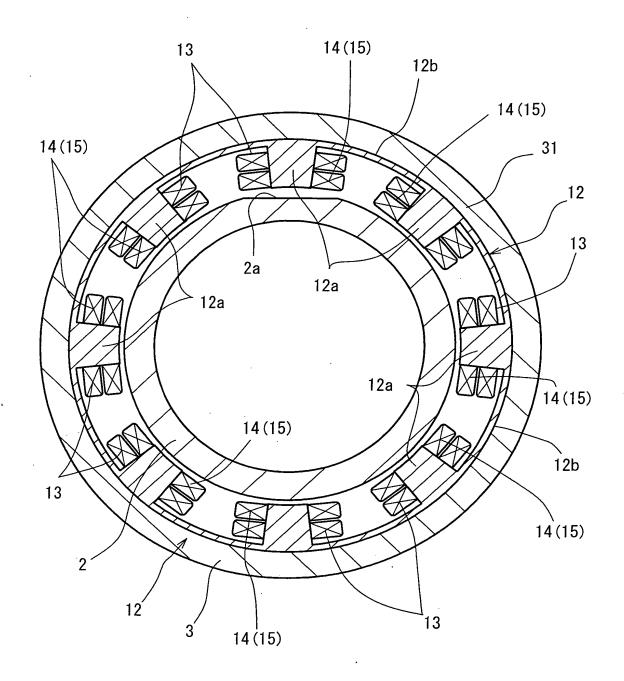
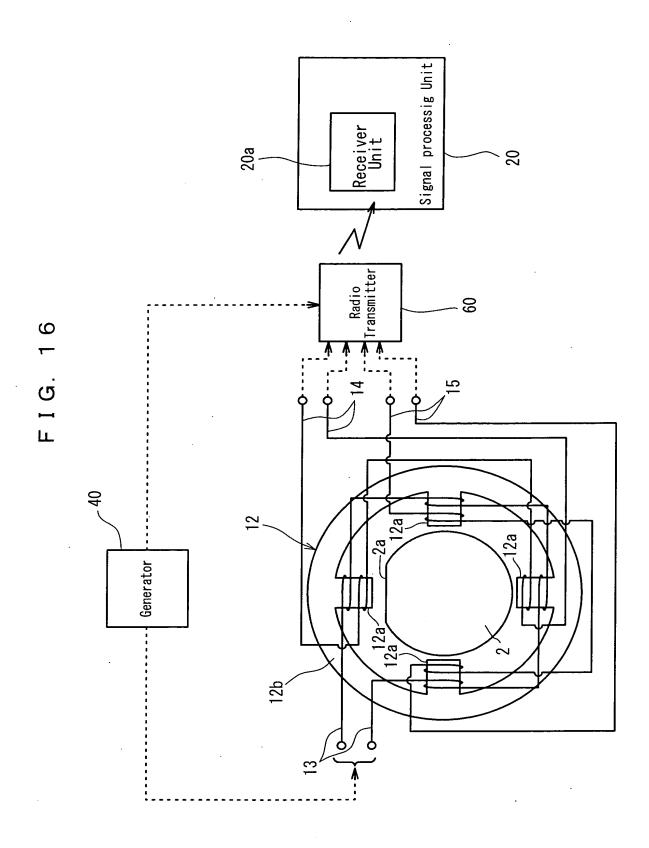


FIG. 15





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FIG. 17

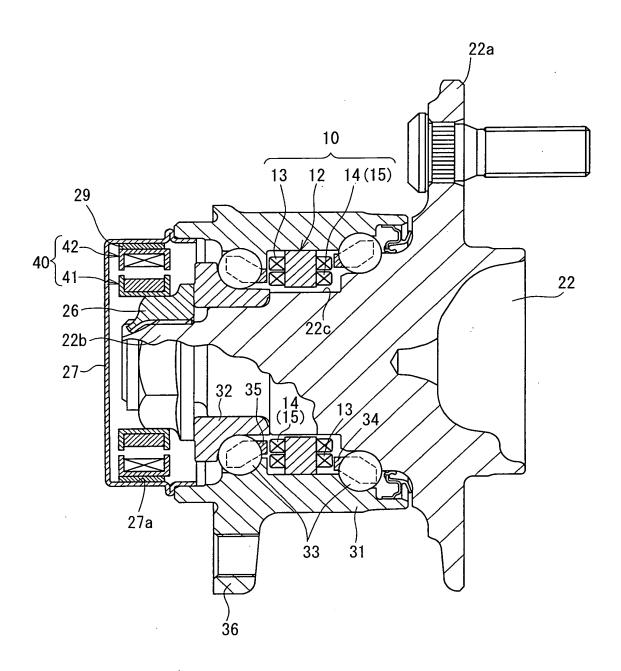


FIG. 18

